



Angela Arapoyanni

Title: Associate Professor

Sector: Communications and Signal Processing

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Academic Qualifications:

- Ph.D. in Optoelectronics (1983) Dept. of Physics, University of Athens
- M.Sc. in Electronics and Computers (1976), Physics Department, University of Athens
- M.Sc. in Electronics and Radioelectrology (1975), Physics Department, University of Athens
- B.Sc. in Physics (1973) Dept. of Physics, University of Athens

Appointments:

- 2002 - today : Associate Professor, Dpt. of Informatics & Telecom., University of Athens
- 1989 - 2002 : Assistant Professor, Dpt. of Informatics & Telecom., University of Athens
- 1988 - 1989 : Assistant Professor, Dpt. of Physics, University of Athens
- 1983 - 1988 : Lecturer, Dpt. of Physics, University of Athens
- 1974 – 1983 :Assistant, Dpt. of Physics, University of Athens

Teaching Activities: (current)

Undergraduate courses

- Electronics
- Microelectronics and IC Design

Postgraduate courses

- CMOS VLSI RF Design
- Analog and Mixed Signal VLSI Design
- Design and Measurement of HF Devices

Supervisor of 8 Ph.D. dissertations

Research Interests/Activities: (last 10 years)

- RF Design, Testing and Calibration
- Low Power Design Techniques
- Testing and Design for Testability of CMOS Integrated Circuits
- CMOS IC Design (Logic Families, Memories, Analog ICs)
- Microelectronic and Optoelectronic Devices.

Scientific Publications/Citations:

- 30 publications in refereed journals
- 77 refereed conference publications
- More than 200 citations.

Top five publications

- "A Comparative Study of Different Current Mode Sense Amplifiers in Submicron CMOS Technology", A. CHRISANTHOPOULOS, Y. Moisiadis, Y. Tsiatouhas and A. Arapoyanni, IEE Proceedings on Circuits, Devices and Systems, vol. 149, no. 3, pp. 154-158, 2002.
- "A Circuit for Concurrent Detection of Soft and Timing Errors in Digital CMOS ICs", S. Matakias, Y. Tsiatouhas, Th. Haniotakis and A. Arapoyanni Journal of Electronic Testing: Theory and Applications, vol. 20, no. 5, pp. 517-525, 2004.
- "A Design Technique for Energy Reduction in NORA CMOS Logic", K. Limniotis, Y. Tsiatouhas, Th. Haniotakis and A. Arapoyanni, IEEE Transactions on Circuits and Systems-I, vol. 53, no. 12, pp. 2647-2655, 2006.
- "A Current Mode, Parallel Two-Rail Code Checker", S. Matakias, Y. Tsiatouhas, Th. Haniotakis and A. Arapoyanni, IEEE Transactions on Computers, vol. 57, No 8, pp. 1032-1045, 2008.
- "A Built-In-Test Circuit for RF Differential Low Noise Amplifiers", I. Dermentzoglou, a. Arapoyanni and y. Tsiatouhas, IEEE Transactions on Circuits and Systems I, VOL. 57, NO 7, pp 1549-1558, July 2010.